

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,812,760 B1
APPLICATION NO. : 10/613302
DATED : November 2, 2004
INVENTOR(S) : Kang Yong Kim and Gary Johnson

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Column 1, Line 49	“the phase-shift”	--the phase shift--
Column 3, Line 17	“VD as a value”	--VD has a value--
Column 3, Line 29	“phase detector 10”	--phase detector 110--
Column 3, Line 60	“each cycle of the”	--at each cycle of the--
Column 3, Line 64	“until the delaylocked”	--until the delay locked--
Column 6, Line 11	“schematic drawing the”	--schematic drawing of the--
Column 7, Line 21	“signal, erratic, and non-linear”	--signal, erratic and non-linear--
Column 7, Line 60	“if rippling present”	--if rippling is present--
Column 8, Line 61	“appreciated, that”	--appreciated that--
Column 9, Line 45	“embodiment the fine/coarse”	--embodiment of the fine/coarse--
Column 10, Line 12	“fine delay. circuit”	--fine delay circuit--
Column 10, Line 54	“shift register having”	--shift register having a--
Column 11, Line 59	“thus, causing”	--thus causing--
Column 12, Line 2	“the SHIF d”	--the SHIFTd--
Column 13, Line 47	“32 bits words”	--32 bit words--
Column 13, Line 63	“received N/2 bits”	--received N/2 bit--
Column 15, Line 18	“such as output”	--such output--

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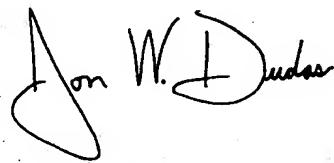
Column 22, Line 18

"by reference"

--by a reference--

Signed and Sealed this

Twenty-first Day of November, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office